

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicants(s): Viswanath Annampedu
Pervez M. Aziz
Case: 8-15
Serial No.: Unassigned
Filing Date: Concurrently Herewith
Group: Unassigned
Examiner: Unassigned

Title: Method and Apparatus for Reducing Leakage Power in a Cache Memory

INFORMATION DISCLOSURE STATEMENT

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, Applicants' attorney wishes to bring to the attention of the Patent and Trademark Office the following document listed on the accompanying PTO Form 1449. A copy of the listed items are enclosed.

1. U.S. Patent Application Serial No. 10/436,526
2. U.S. Patent Application Serial No. 10/676,560

The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made, or as an admission that the information cited is considered to be material to patentability or that no other material information exists.

Respectfully submitted,



Date: April 14, 2004

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